Patent Assignment Abstract of Title

Total Assignments: 1

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Inventor: Sang-Young Noh

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Assignment: 1

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Search Results as of: 10/2/2003 4:15:59 P.M.

L Number	Hits	Search Text	DB	Time stamp
-	0	(wcdma or (w adj cdmad)or w-cdma) and (separation adj type adj terminal)	USPAT; US-PGPUB	2003/09/30 15:58
-	0	(wcdma or (w adj cdma)or w-cdma) and (separation adj type adj terminal)	USPAT; US-PGPUB	2003/09/30 15:57
-	1	cdma and (separation near terminal)	USPAT; US-PGPUB	2003/09/30 15:58
-	0	wcdma and (separation near terminal)	USPAT; US-PGPUB	2003/09/30 15:58
-	130	wcdma and separation	USPAT; US-PGPUB	2003/09/30 15:58
-	238	(wcdma or (w adj cdma)or w-cdma) and separation	USPAT; US-PGPUB	2003/09/30 16:05
-	16	((wcdma or (w adj cdma)or w-cdma) and WLL) and 370/$.ccls.	USPAT; US-PGPUB	2003/09/30 16:01
-	1	((wcdma or (w adj cdma)or w-cdma) and WLL) and (basic near type)	USPAT; US-PGPUB	2003/09/30 16:06
-	38	(wcdma or (w adj cdma)or w-cdma) and WLL	USPAT; US-PGPUB	2003/09/30 16:07
-	6	((wcdma or (w adj cdma)or w-cdma) and separation) and WLL	USPAT; US-PGPUB	2003/09/30 16:15
-	21	(TDM or E1)with (Master adj slave)	USPAT; US-PGPUB	2003/09/30 16:19
-	12	((TDM or E1)with (Master adj slave)) and @py<=1999	USPAT; US-PGPUB	2003/09/30 16:28
-	187	microprocessor and framer and register and decoder	USPAT; US-PGPUB	2003/09/30 16:41
-	6293786	(microprocessor and framer and register and decoder) @py<1999	USPAT; US-PGPUB	2003/09/30 16:30
-	58	(microprocessor and framer and register and decoder) and @py<1999	USPAT; US-PGPUB	2003/09/30 16:30
-	48	((microprocessor and framer and register and decoder) and @py<1999) and 370/$	USPAT; US-PGPUB	2003/09/30 16:35
-	327	framer and register and decoder	USPAT; US-PGPUB	2003/09/30 16:50
-	91	(framer and register and decoder) and (370/$.ccls. and @py<=1999)	USPAT; US-PGPUB	2003/09/30 16:43
-	16	((framer and register and decoder) and (370/$.ccls. and @py<=1999)) and $cdma	USPAT; US-PGPUB	2003/09/30 16:44
-	4	(multiplexer with tdm with framing) and (370/$.ccls. and @py<=1999)	USPAT; US-PGPUB	2003/09/30 16:56
-	2800	(tdm mux) and (370/$.ccls. and @py<=1999)	USPAT; US-PGPUB	2003/09/30 17:11
-	85	((tdm mux) and (370/$.ccls. and @py<=1999)) and E1 and framing	USPAT; US-PGPUB	2003/09/30 17:00
-	615	((tdm mux) and (370/$.ccls. and @py<=1999)) and framing	USPAT; US-PGPUB	2003/09/30 17:01
-	62	(((tdm mux) and (370/$.ccls. and @py<=1999)) and framing) and (master near slave)	USPAT; US-PGPUB	2003/09/30 17:09
-	11912	serial and parallel and register and frame and logic	USPAT; US-PGPUB	2003/09/30 17:10
-	1381	E1 and frame and @py<=1990	USPAT; US-PGPUB	2003/09/30 17:12
-	11	(E1 and frame and @py<=1990) and (master near slave)	USPAT; US-PGPUB	2003/09/30 17:18